



☐ Drafts
☐ Pending
☒ Active

- ☒ L14: (1) 6308312.pn.
- ☒ L15: (1) (gate near9 mode) and 114
- ☒ L16: (402) vdd near9 (ground or gnd) near9 connect\$4 near9 gate
- ☒ L17: (20496) gate near9 mode
- ☒ L18: (59) 116 and 117
- ☒ L19: (52) low and high and 118
- ☒ L20: (95701) control\$4 same threshold
- ☒ L21: (22) 119 and 120
- ☒ L22: (33321) leakage and current and transistor
- ☒ L23: (14) 121 and 122
- ☒ L24: (8) (leakage near5 current) and 123

☐ Failed

☐ Saved

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☒ Plurals

☒ Highlight all hit terms initially

(leakage near5 current) and 123

	U	1	Document ID	Issue Date	Page	Title	Current
1	<input checked="" type="checkbox"/>	<input type="checkbox"/>	US 20010050379 A	20011213	16	Low power consuming circuit	257/200
2	<input type="checkbox"/>	<input type="checkbox"/>	US 6476641 B2	20021105	15	Low power consuming circuit	326/83
3	<input checked="" type="checkbox"/>	<input type="checkbox"/>	US 6429689 B1	20020806	10	Method and apparatus for controlling both ac	326/95
4	<input checked="" type="checkbox"/>	<input type="checkbox"/>	US 6333571 B1	20011225	22	MOS integrated circuit device operating with	307/125
5	<input checked="" type="checkbox"/>	<input type="checkbox"/>	US 6097113 A	20000801	23	MOS integrated circuit device operating with	307/125
6	<input checked="" type="checkbox"/>	<input type="checkbox"/>	US 6060905 A	20000509	35	Variable voltage, variable impedance CMOS of	326/81
7	<input checked="" type="checkbox"/>	<input type="checkbox"/>	US 5835420 A	19981110	45	Node-precise voltage regulation for a MOS me	365/189
8	<input checked="" type="checkbox"/>	<input type="checkbox"/>	US 5617062 A	19970401	51	Timing circuit with rapid initialization on	331/111

Start



4:44 PM